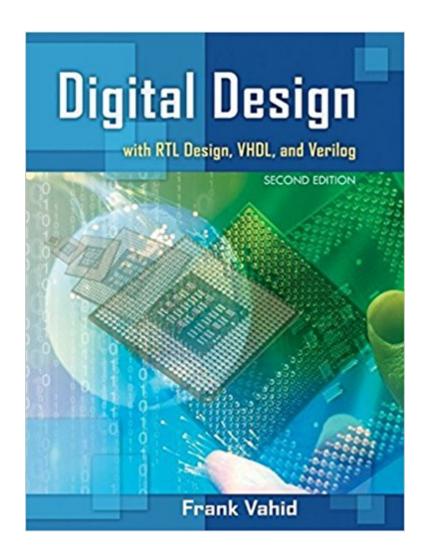


## The book was found

# Digital Design With RTL Design, VHDL, And Verilog





### Synopsis

An eagerly anticipated, up-to-date guide to essential digital design fundamentals Offering a modern, updated approach to digital design, this much-needed book reviews basic design fundamentals before diving into specific details of design optimization. You begin with an examination of the low-levels of design, noting a clear distinction between design and gate-level minimization. The author then progresses to the key uses of digital design today, and how it is used to build high-performance alternatives to software. Offers a fresh, up-to-date approach to digital design, whereas most literature available is sorely outdated Progresses though low levels of design, making a clear distinction between design and gate-level minimization a clear distinction between design and gate-level minimization. The book by your side, you'll gain a better understanding of applying digital design to your life With this book by your side, you'll gain a better understanding of how to apply the material in the book to real-world scenarios.

### **Book Information**

Hardcover: 594 pages Publisher: Wiley; 2 edition (March 9, 2010) Language: English ISBN-10: 0470531088 ISBN-13: 978-0470531082 Product Dimensions: 7.4 x 1 x 9.3 inches Shipping Weight: 2.2 pounds (View shipping rates and policies) Average Customer Review: 4.2 out of 5 stars 20 customer reviews Best Sellers Rank: #31,394 in Books (See Top 100 in Books) #2 in Books > Computers & Technology > Hardware & DIY > Microprocessors & System Design > Computer Design #17 in Books > Engineering & Transportation > Engineering > Electrical & Electronics > Digital Design #216 in Books > Computers & Technology > Computer Science

### **Customer Reviews**

I paid a lot of money for this book. It's nice, well written, but only a rehash of what I already know after 25 years of digital chip design. I was hoping that what calls it "Digital System Design with SystemVerilog" would be what I received. I should have looked at the "look inside" section more closely. Check out the title of the book - "Digital Design with RTL Design, VHDL, and Verilog" - no mention of SystemVerilog! I'm still keeping it because it's a good reference and it's a lot newer than my Palnitkar and Bhasker Verilog books. I really think I got baited and switched by the description.

Nonetheless, if you're a student or have less than 10 years experience coding RTL, I think this book would be good. I also ordered SystemVerilog for Design and Verilog and SystemVerilog Gotchas by Sutherland et al and find those to be much more what I needed. I find that in general, while the Springer publisher books are very expensive and generally small, they are a lot more directed to experienced designers. I don't work for Springer and I don't know Sutherland so I hope you believe this is a sincere review. I just don't want other people throwing away money on something they already know.

great book for beginners like me

Absolutely pristine condition. No marks, scratches, corner bumps, etc. Great product, Delivered on time and was exactly what was shown. Although my professor hardly used the book during lectures, came in handy when teaching myself beginning VHDL coding.

If you're taking EECS31 (intro to digital systems) at a U.C. this is the correct book

This book as great examples and is very readable

It was the correct book for my class. That's about all I can say about it.

Super speedy shipping. Exactly as described. Thank you!

It is an excellent textbook, colorful description, examples well explained. The part of reading is interesting because it tell you how it works this stuff in the real life

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